INDEX

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Ex.  No. | Date | Title | Page | Signature |
| 1 |  | Microprocessor 8086 |  |  |
| 2 |  | X-OR gate |  |  |
| 3 |  | One bit adder |  |  |
| 4 |  | 8 – bit adder |  |  |
| 5 |  | 8 – bit subractor |  |  |
| 6 |  | Tiny ALU |  |  |
| 7 |  | Computer Components |  |  |
| 8 |  | Simulation of Instruction level Parallelism |  |  |
| 9 |  | Data level Parallelism |  |  |
| 10 |  | Pipeline |  |  |
| 11 |  | Cache |  |  |
| 12 |  | Multiprocessor |  |  |
| 13 |  | Vector Processor |  |  |
| 14 |  | Thread level Parallelism |  |  |